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<b>Title</b>	The use of Synchronous languages in Hardware/Software Co-Design Systems
<b>Abstract</b>	<p>A key issue in modern systems is the design, specification, development and verification of Hardware/Software Co-Design; particularly those involving highly concurrent implementations. This research will focus on hardware descriptions in synchronous languages, and how these relate to high level safety and systems issues.</p> <p>Investigation take place into the languages, tools and techniques necessary to relate high level specifications to low level implementations and synthesis of FPGA-based systems.</p>